AMENDMENTS TO THE CLAIMS

The following Listing of Claims will replace all prior versions, and Listing of Claims in the Application:

LISTING OF CLAIMS:

1. (Original) A driving circuit configured in a three-phase inverter, comprising:

a first switch assembly including a first high-side switch connected between an input voltage and a first node, and a first low-side switch connected between said first node and a reference voltage;

a second switch assembly including a second high-side switch connected between said input voltage and a second node, and a second low-side switch connected between said second node and said reference voltage;

a third switch assembly including a third high-side switch connected between said input voltage and a third node, and a third low-side switch connected between said third node and said reference voltage; and

a three-phase transformer having a primary side with three terminals connected with said first, second and third nodes, respectively, and a secondary side with three terminals connected with a first, second and third loadings, respectively;

wherein said switches are switched for generating a first AC voltage between said first and second nodes, a second AC voltage between said second and third nodes, and a third AC voltage between said third and first nodes, respectively, so as to be transformed by said three-phase transformer to generate a first AC current for said first loading, a second AC current for said second loading, and a third AC current for said third loading, respectively.

2. (Original) The driving circuit according to claim 1, wherein said three-phase transformer comprises two transformers connected in series.

3. (Original) The driving circuit according to claim 1, wherein said three-phase transformer comprises three transformers connected in Y-Y configuration.

4. (Original) The driving circuit according to claim 1, wherein

said three-phase transformer comprises three transformers connected in Δ-Δ configuration.

5. (Original)The driving circuit according to claim 1, wherein said three AC voltages have a phase difference of 120 degrees between each two of them.

6. (Original) The driving circuit according to claim 1, wherein said three AC currents have a phase difference of 120 degrees between each two of them.

7. (Original) The driving circuit according to claim 1, wherein said switches each is connected with a diode in parallel.

8. (Original) The driving circuit according to claim 1, wherein said switches each comprises an NMOS transistor.

9. (Original) The driving circuit according to claim 1, wherein said input voltage is a DC voltage.

10. (Original) The driving circuit according to claim 1, wherein

said three loadings each includes at least one cold cathode fluorescent lamp.

11. (Original) A driving method comprising the steps of:

connecting a first switch assembly including a first high-side and low-side switches connected in series between an input voltage and a reference voltage;

connecting a second switch assembly including a second high-side and low-side switches connected in series between said input voltage and reference voltage;

connecting a third switch assembly including a third high-side and low-side switches connected in series between said input voltage and reference voltage;

switching said high-side and low-side switches for generating three AC voltages; and

transforming said three AC voltages to three AC currents each for one of three loadings.

12. (Original) The method according to claim 11, further comprising modulating said three AC voltages to have a phase difference of 120 degrees between each two of them.

13. (Original) The method according to claim 11, further comprising modulating said three AC currents to have a phase difference of 120 degrees between each two of them.

14. (Original) The method according to claim 11, further comprising driving at least one cold cathode fluorescent lamp by each of said three AC currents.

Claims 15-17 (Canceled).